Notice of References Cited Application/Control No. 10/570,839 Examiner MATTHEW W. SUCH Applicant(s)/Patent Under Reexamination DOLL ET AL. Art Unit Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-6,433,357	08-2002	Kuijk et al.	257/40
*	В	US-2003/0080426	05-2003	Klauk et al.	257/744
*	O	US-7,435,992	10-2008	Choi et al.	257/72
*	D	US-3,369,159	02-1968	PARKER SIDNEY G et al.	257/288
	Е	US-			
	F	US-			
	G	US-			
	Ι	US-			
	_	US-			
	J	US-			
	K	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Ø					
	R					
	Ø					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
	υ	Garnier, F., et al. "All-Polymer Field-Effect Transistor Realized by Printing Techniques." SCIENCE, Vol. 265 (1994): pp. 1684-1686.		
	\ \	Garnier, F., et al. "Vertical Device Architecture by Molding of Organic-Based Thin Film Transistor." APPL. PHYS. LETT., Vol. 73 (1998): pp. 1721-1723.		
	w			
	х			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.